

Curvature Wavefront Sensor Arbitrary Waveform Generator

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Subaru Telescope (NAOJ)

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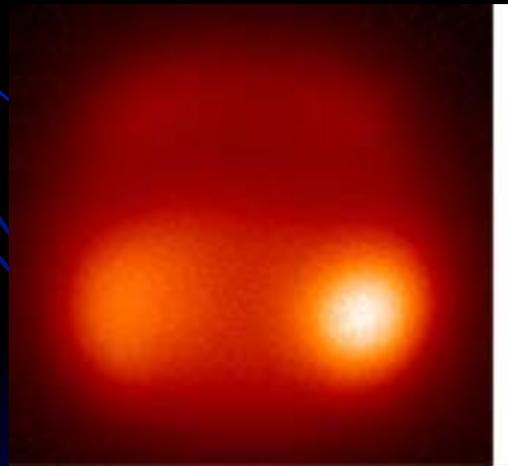
Subaru Adaptive Optics

Without AO

Earth's atmosphere
distorts light



Telescope
images blurred

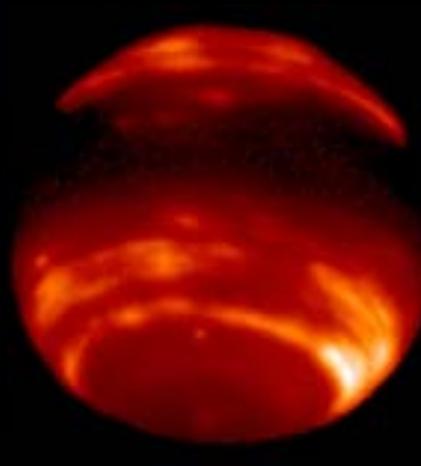


With AO

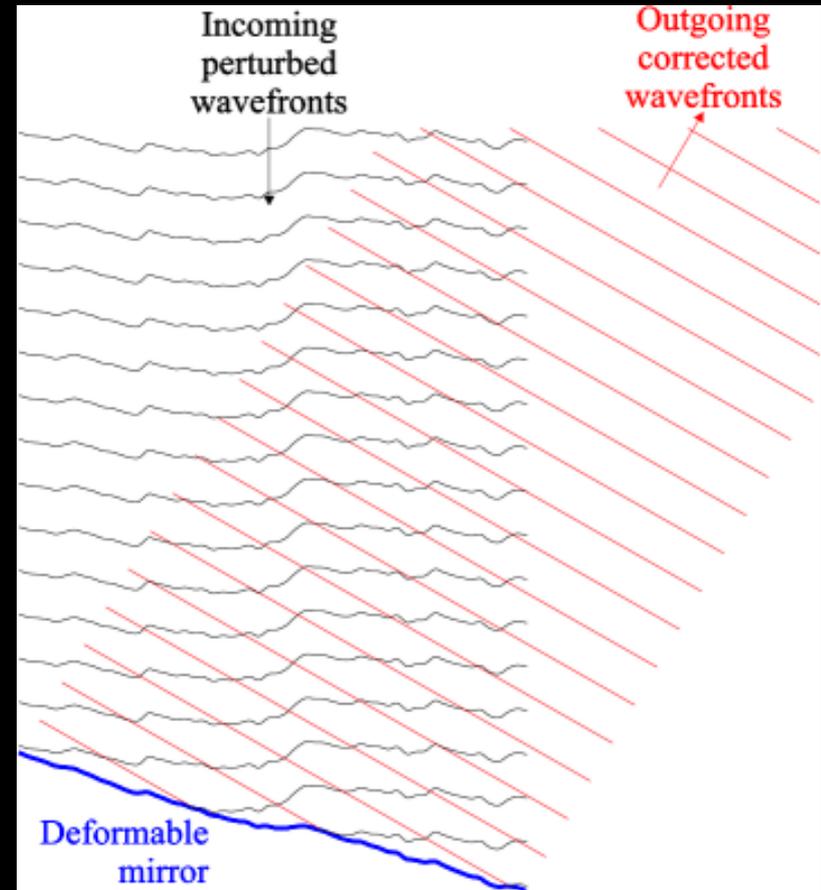
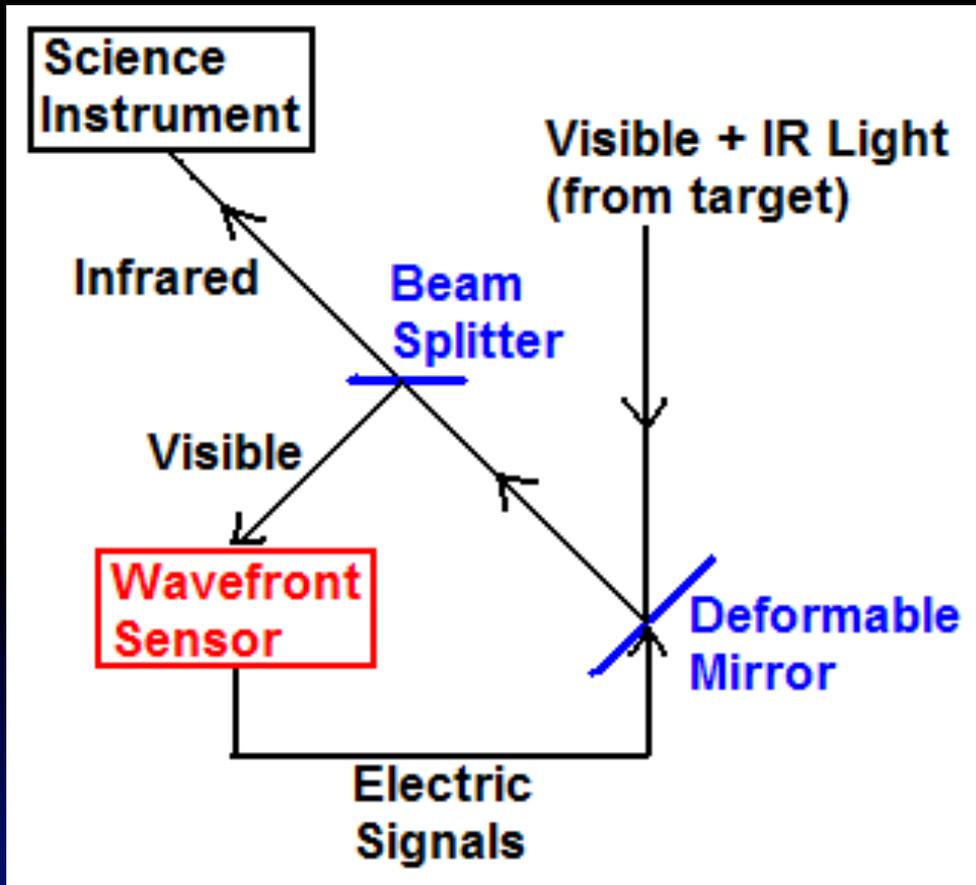
Removes effects
of atmospheric
distortion



Image resolution
improved



AO System Components



Wavefront Sensor

Visible light reflects off *one side* of membrane



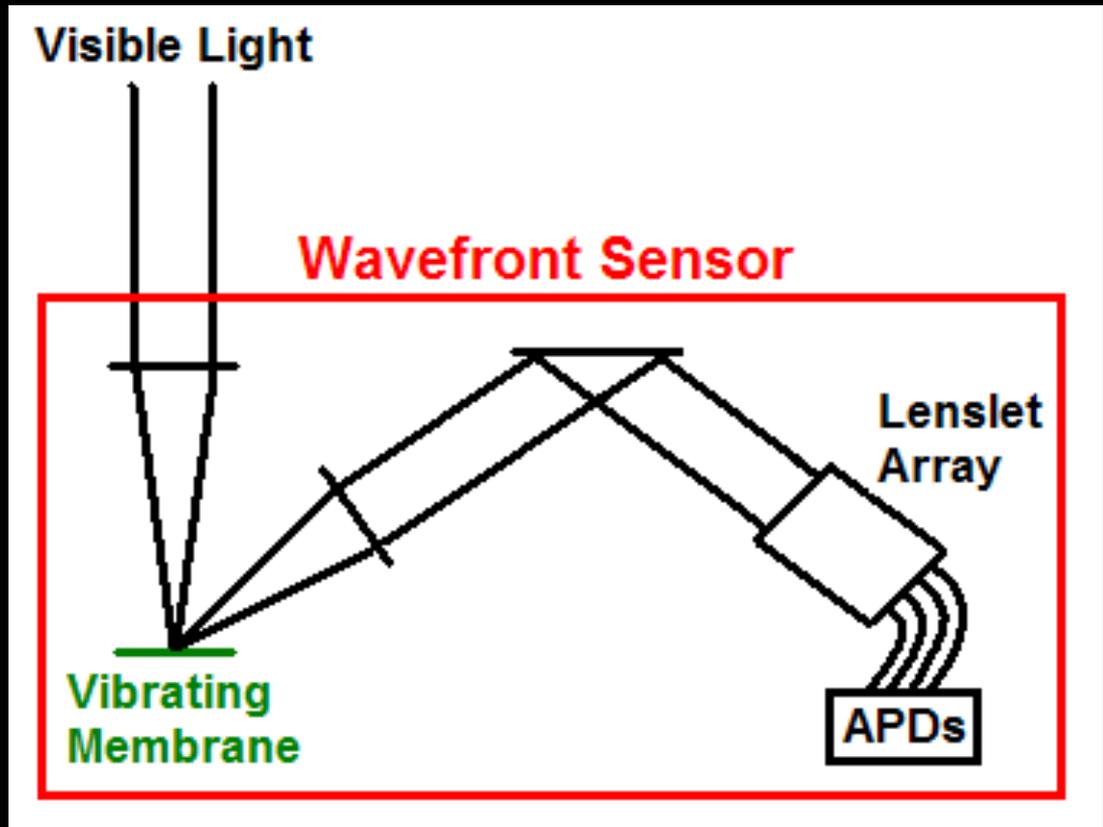
APDs count photons in each lenslet



Measure light intensity

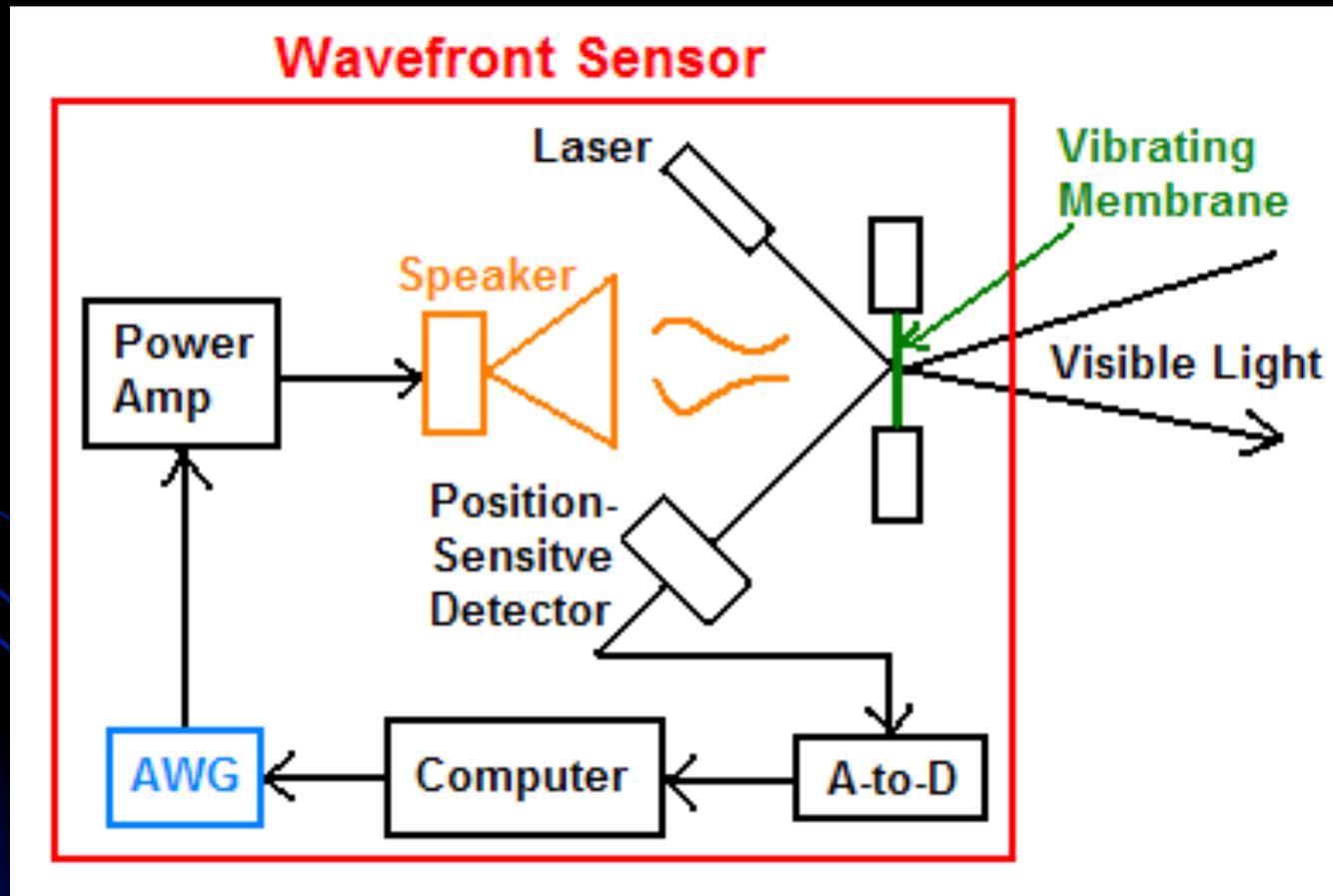


Calculate wavefront phase shift

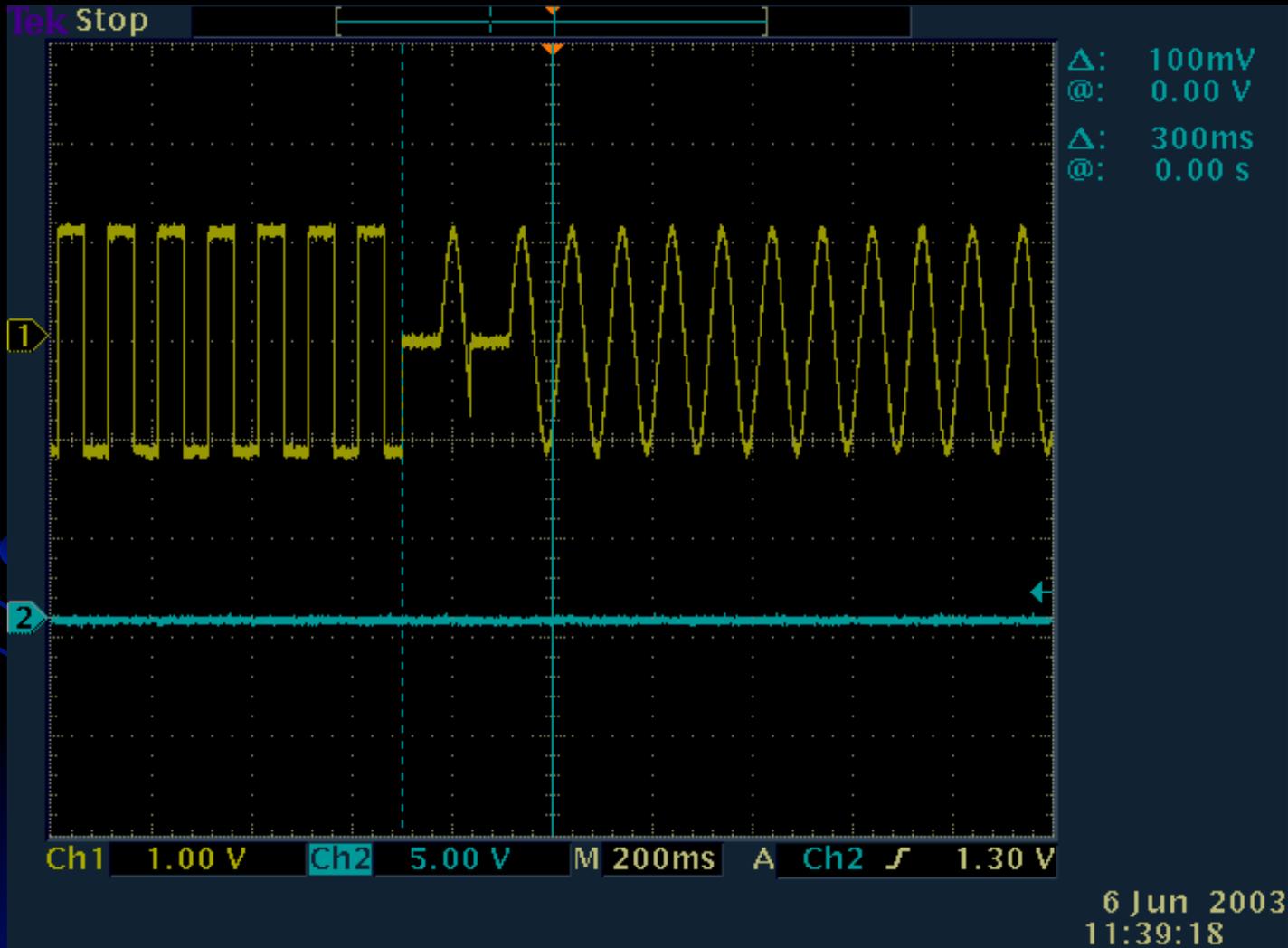


What vibrates membrane?

Other side of membrane

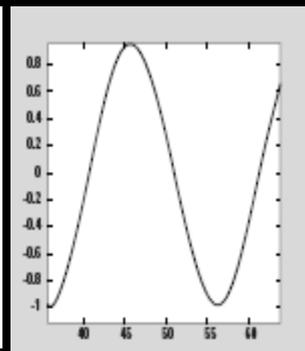
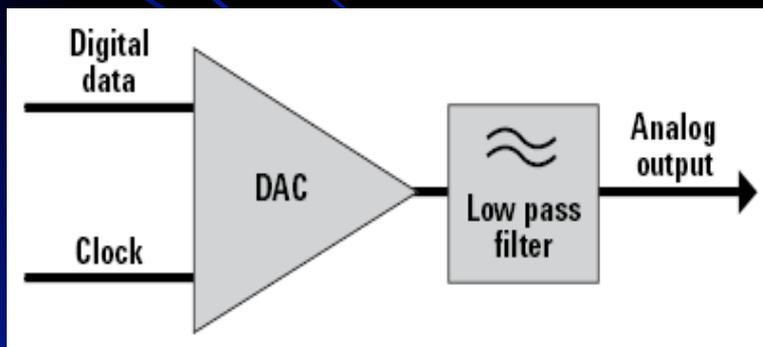
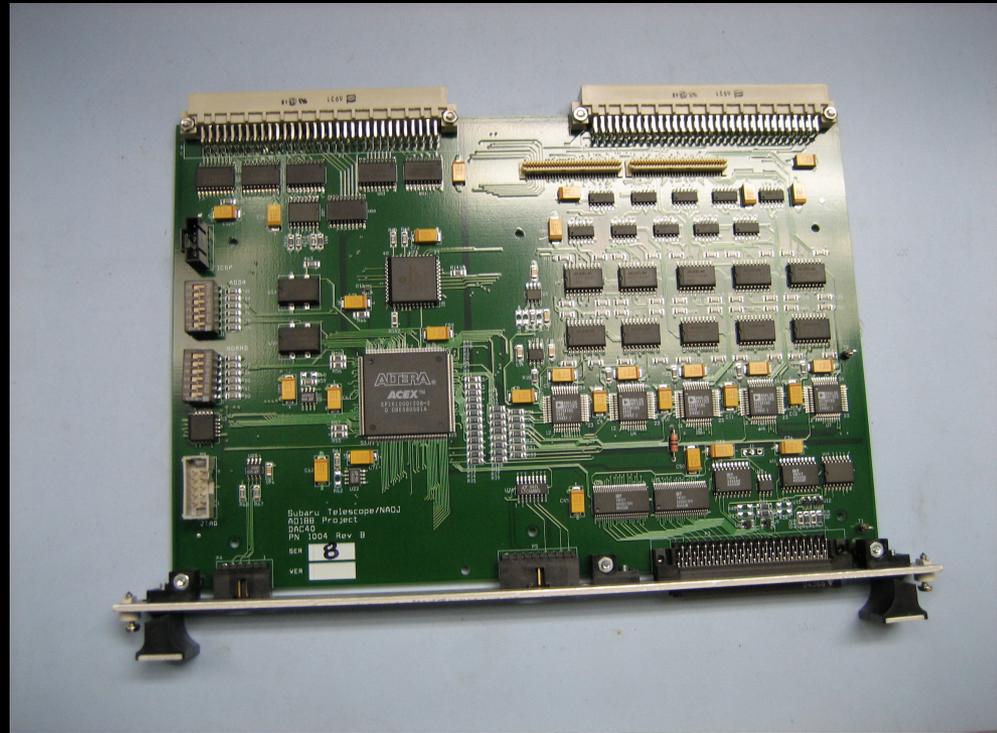


Problem



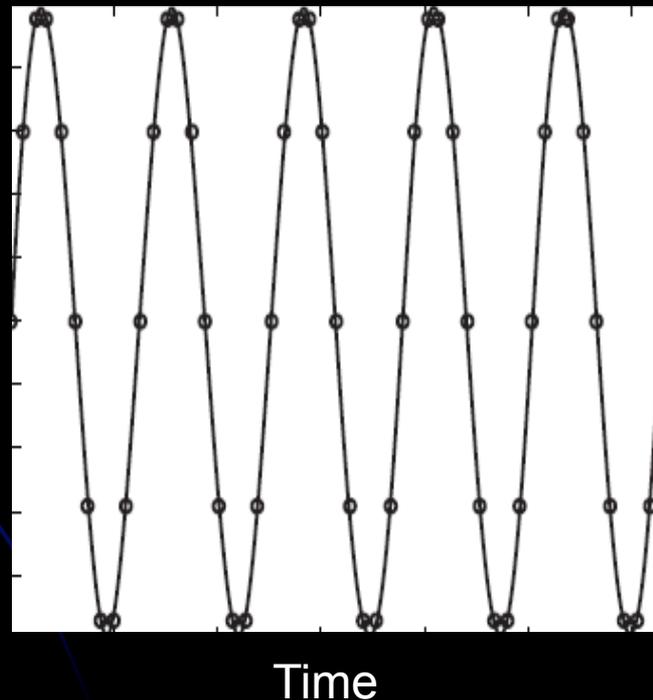
Solution

- Design your own AWG!
- Use DAC40 board
 - Field Programmable Gate Array (FPGA)
 - Digital-to-Analog Converters (DACs)

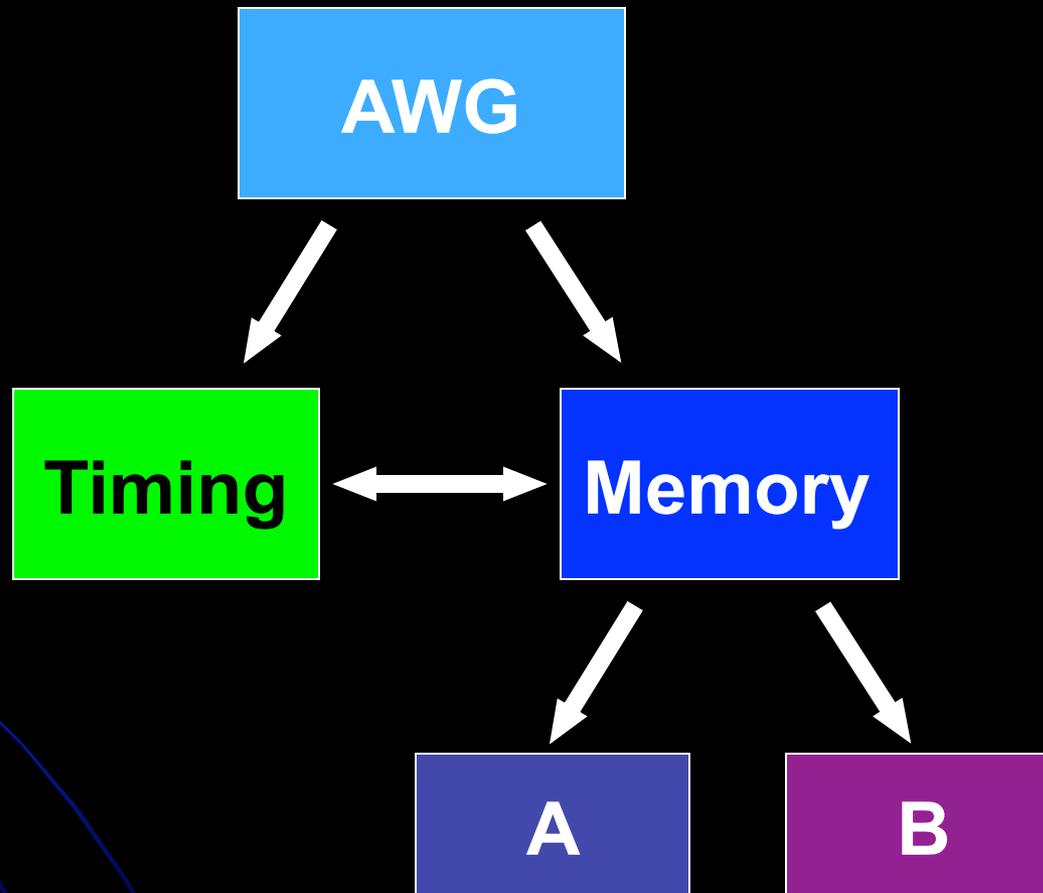


AWG Specification

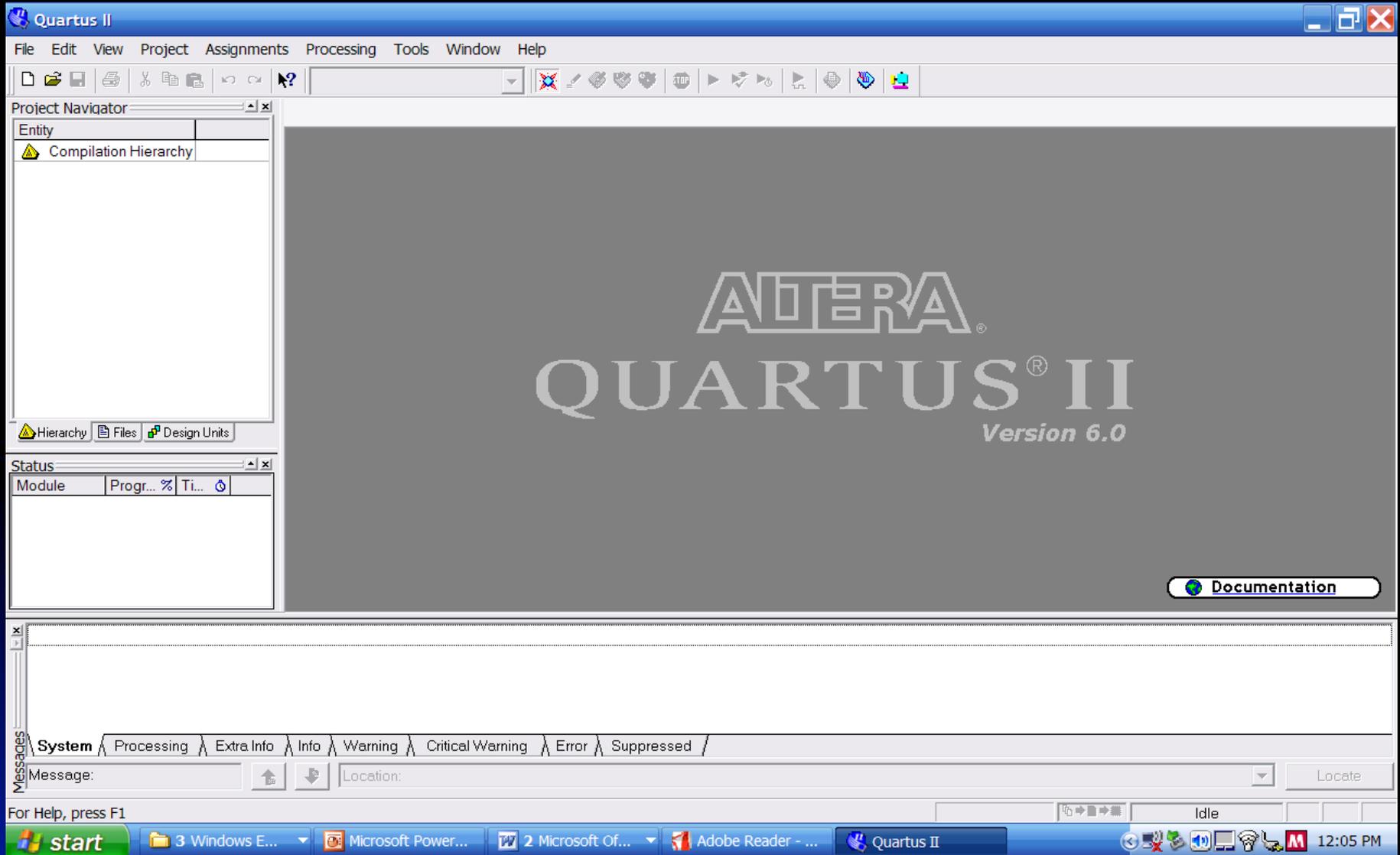
- Range of period – 100 Hz to 4 kHz
- Number of points – 1000
- Voltage resolution – 14 bits (DAC40 DACs)
- Update rate for new waveforms – 0.1 seconds (10 Hz)



Logic Block Diagram



Implement using Quartus II



Design Flow

Design Entry
(VHDL)



Compilation



Simulation



Program
Device

```
25
26 ARCHITECTURE behave OF awgmemory IS
27   SIGNAL sclr      : STD_LOGIC;
28   SIGNAL CT       : STD_LOGIC;
29   SIGNAL address  : address_bus;
30   SIGNAL dataA    : data_bus;
31   SIGNAL dataB    : data_bus;
32   SIGNAL data     : data_bus;
33   SIGNAL reg_enable : STD_LOGIC;
34   --timing signals
35   SIGNAL count_out : integer RANGE 0 TO 2047 := 1601; --1600 clock cycles
36   SIGNAL npcount   : integer RANGE 0 TO 127 := 102;  --101 next_pts
37   SIGNAL BOC_count : integer RANGE 0 TO 15 := 6;    --5 BOCs
38   SIGNAL next_pt   : STD_LOGIC;
39   SIGNAL BOC       : STD_LOGIC;
40   SIGNAL wfpulse   : STD_LOGIC;
41   SIGNAL waveform  : STD_LOGIC;
42 BEGIN
43   counter : lpm_counter
44     GENERIC MAP (LPM_WIDTH => 10)
45     PORT MAP (clock => clk, sclr => sclr, cnt_en => CT, q => address);
46   memoryA : altddram
47     GENERIC MAP (WIDTH => 14, WIDTHAD => 10, RDADDRESS_REG => "UNREGISTERED",
48                 LPM_FILE => "memoryA.mif")
49     PORT MAP (data => datainA, rdaddress => address, wraddress => wraddressA,
50             wren => wren, q => dataA, inclock => clk);
51   memoryB : altddram
52     GENERIC MAP (WIDTH => 14, WIDTHAD => 10, RDADDRESS_REG => "UNREGISTERED",
53                 LPM_FILE => "memoryB.mif")
54     PORT MAP (data => datainB, rdaddress => address, wraddress => wraddressB,
55             wren => wren, q => dataB, inclock => clk);
56   reg : lpm_ff
57     GENERIC MAP (LPM_WIDTH => 14)
58     PORT MAP (data => data, clock => clk, enable => reg_enable, q => dac_data);
59
60   --GENERATE TIMING
61   --next_pt counter - count 1600 clocks
62   PROCESS (clk, count_out)
63   BEGIN
64     IF (clk = '1') AND (clk'EVENT) THEN
65       IF (count_out = 1601) THEN
66         next_pt <= '1';
67         count_out <= count_out - 1;
68       ELSIF (count_out = 1) THEN
69         count_out <= 1600;
70         next_pt <= '1';
71       ELSE
72         count_out <= count_out - 1;
73         next_pt <= '0';
74       END IF;
75     END IF;
76   END PROCESS;
77
78   --BOC counter - count 101 next_pts
79   PROCESS (next_pt, npcount)
80   BEGIN
81     IF (next_pt = '1') AND (next_pt'EVENT) THEN
82       IF (npcount = 102) THEN
```

Simulate Design

Design Entry
(VHDL)



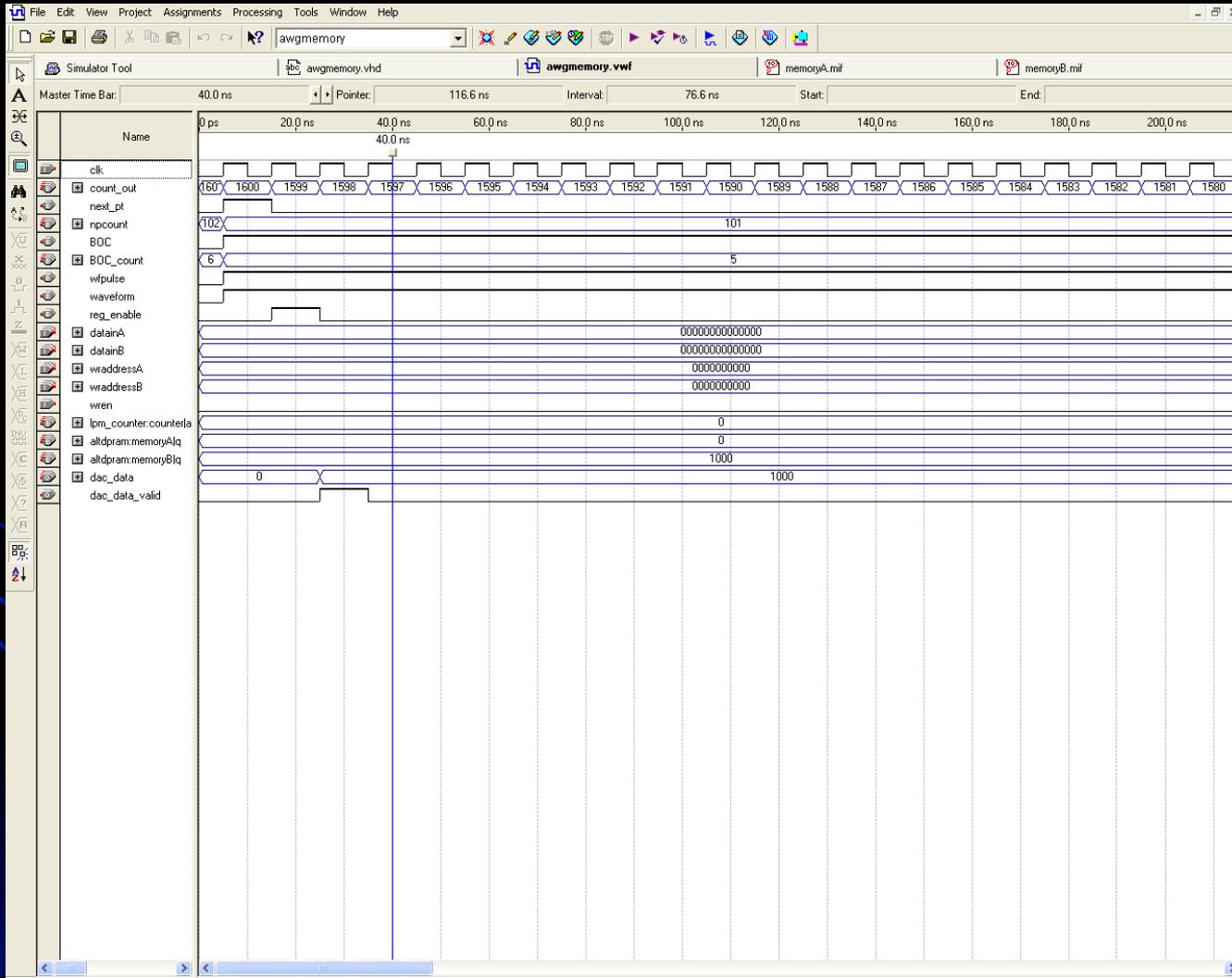
Compilation



Simulation



Program
Device



Conclusion

Dual-stroke waveform concept

- Efficient – Only 1 wavefront sensor
- Astronomical objects appear 2.5X brighter

In the Future

- Interface DAC40 to oscilloscope
- Test AWG
- Implement in next generation AO system

Acknowledgements

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